# MORNSUN®

10W isolated DC-DC converter in DIP package Ultra-wide input and regulated dual/single output



# FEATURES

- Ultra-wide 4:1 input voltage range
- High efficiency up to 87%
- No-load power consumption as low as 0.12W
- I/O isolation test voltage 3k VDC
- Input under-voltage protection, output short circuit, over-current, over-voltage protection
- Operating ambient temperature range: -40℃ ~ +85℃
- Meets CISPR32/EN55032 CLASS A, without extra components
- Input reverse polarity protection available with Chassis (A2S) or 35mm DIN-Rail mounting (A4S) version
- IEC60950, UL60950, EN60950 approved
- Industry standard pin-out

URE\_LP-10WR3 & URF\_LP-10WR3 series of isolated 10W DC-DC converter products with an ultra-wide 4:1 input voltage range and feature efficiencies of up to 87%, input to output isolation is tested with 3000VDC and the converters safely operate in an ambient temperature of -40°C to +85°C, input under-voltage protection, output short-circuit, over-current and over-voltage protection. They meet CLASS A of CISPR32/EN55032 EMI standards without external components, optional packages A2S and A4S also offer the added feature of input reverse polarity protection and they are widely used in applications for industrial control, electric power, instruments and communication fields.

		Input Voltage (VDC)		Output		Full Load	Max.
Certification	Part No. $^{\circ}$	Nominal <sup>®</sup> (Range)	Max. <sup>3</sup>	Voltage (VDC)	Current (mA) Max./Min.	Efficiency®(%) Min./Typ.	Capacitive Load(µF) <sup>®</sup>
	URE2405LP-10WR3		_	±5	±1000/0	79/81	1000
	URE2412LP-10WR3			±12	±416/0	83/85	330
	URE2415LP-10WR3			±15	±333/0	85/87	220
	URF2403LP-10WR3		40	3.3	2400/0	76/78	5400
	URF2405LP-10WR3	24 (9-36)		5	2000/0	80/82	5400
	URF2409LP-10WR3			9	1111/0	82/84	680
UL/CE/CB	URF2412LP-10WR3			12	833/0	82/84	470
	URF2415LP-10WR3			15	667/0	85/87	330
	URF2424LP-10WR3			24	416/0	84/86	100
	URE4805LP-10WR3		80	±5	±1000/0	80/82	1000
	URE4812LP-10WR3			±12	±416/0	84/86	330
	URE4815LP-10WR3			±15	±333/0	85/87	220
	URF4803LP-10WR3	48		3.3	2400/0	77/79	5400
	URF4805LP-10WR3	(18-75)		5	2000/0	80/82	5400
UL/CE	URF4812LP-10WR3			12	833/0	84/86	470
	URF4815LP-10WR3			15	667/0	85/87	330
-	URF4824LP-10WR3			24	416/0	85/87	100

Notes:

(1)Use "A2S" suffix for chassis mounting and "A4S" suffix for DIN-Rail mounting;

②Minimum input voltage and start-up voltage are increased by 1VDC for all models with A2S and A4S suffixes because of the input reverse polarity function;
③Exceeding the maximum input voltage may cause permanent damage;

(1) Efficiency is measured at nominal input voltage and rated output load; efficiencies for A2S and A4S Model's is decreased by 2% due to the input reverse polarity protection circuit;

(5) The specified maximum capacitive load value for Vo1 and Vo2 output is identical.



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# DC/DC Converter URE\_LP-10WR3 & URF\_LP-10WR3 Series



Item	Operating Conditions		Min.	Тур.	Max.	Unit
	24VDC nominal input series, nominal input voltage	3.3VDC output		423/5	434/12	mA
Innut Current (full lead / no lead)		Others		514/5	527/12	
Input Current (full load / no-load)	48VDC nominal input series, nominal input voltage	3.3VDC output		208/5	214/12	
		Others		254/5	260/12	
Reflected Ripple Current	24VDC nominal input series, n	ominal input voltage		40		
kellected kipple cuttern	48VDC nominal input series, n	ominal input voltage		30		
Surge Voltage (1sec. max.)	24VDC nominal input series		-0.7		50	VDC
suige voliage (1sec. max.)	48VDC nominal input series		-0.7		100	
Start-up Voltage	24VDC nominal input series				9	
sidir-up volidge	48VDC nominal input series				18	
Under-voltage Protection	24VDC nominal input series		5.5	6.5		
under-volidge Florechon	48VDC nominal input series		12	15.5		
Start-up Time	Nominal input voltage & constant resistance load			10		ms
Input Filter				Pit	filter	
Hot Plug			Unavailable			
	Module on		Ctrl pin open or pulled high (3.5-12VDC)			
Ctrl*	Module off		Ctrl pin pulled low to GND (0-1.2VDC)			2VDC)
	Input current when off			5	8	mA

Note: \* The Ctrl pin voltage is referenced to input GND.

Item	Operating Conditions		Min.	Тур.	Max.	Unit
Voltage Accuracy <sup>10</sup>	0% -100% load			±1	±3	
Line on De anderkien	Input voltage variation from	Vo1		±0.2	±0.5	%
Linear Regulation	low to high at full load	Vo2		±0.5	±1.0	
L	5% -100% load	Vo1		±0.5	±l	
Load Regulation <sup>®</sup>		Vo2		±0.5	±1.5	
Cross Regulation	Vo1 load at 50%, Vo2 load at range of 10%-100%				±5	1
Transient Recovery Time				300	500	μs
Transient Response Deviation	25% loaa step change, nomin	25% load step change, nominal input voltage		±3	±5	%
Temperature Coefficient	Full load				±0.03	%/℃
Ripple & Noise®	20MHz bandwidth, 5% -100% l	oad		60	120	mV p-p
Over-voltage Protection				130	160	%Vo
Over-current Protection	Input voltage range		110	140	190	%lo
Short-circuit Protection				Continuous,	self-recovery	,

Note:

0 Output voltage accuracy of ±5VDC output for 0%-5% load is ±5% max;

 $\textcircled{2}\$  Load regulation for 0% -100% load increases to ±5%;

 $\Im$  Ripple & Noise at  $\leq$  5% load is 5% Vo max. The "parallel cable" method is used for Ripple and Noise test, please refer to DC-DC Converter Application Notes for specific information.

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ltem	Operating Conditions	Min.	Typ.	Max.	Unit
Isolation	Input-output Electric Strength Test for 1 minute with a leakage current of 1mA max.	3000			VDC
Insulation Resistance	Input-output resistance at 500VDC	1000			MΩ
Isolation Capacitance	Input-output capacitance at 100KHz/0.1V		500		pF
Operating Temperature	See Fig. 1	-40		+85	Ċ
Storage Temperature		-55		+125	Ľ
Storage Humidity	Non-condensing	5		95	%RH
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds			+300	C
Vibration		10-5	5Hz, 2G, 30 N	in. along X, Y	and Z
Switching Frequency*	PWM mode		350		KHz
MTBF	MIL-HDBK-217F@25°C	1000			K hours

Note:\* Switching frequency is measured at full load. The module reduces the switching frequency for light load (below 50%) efficiency improvement.

Mechanical Specifications					
Case Material	Black plastic; flame-retardant and heat-resistant (UL94 V-0)	Black plastic; flame-retardant and heat-resistant (UL94 V-0)			
	Horizontal package	51.50 x 26.50 x 12.00 mm			
Dimensions	A2S chassis mounting	76.00 x 31.50 x 21.20 mm			
	A4S Din-rail mounting	76.00 x 31.50 x 25.80 mm			
Weight	Horizontal package/A2S chassis mounting/A4S Din-rail mounting	21.2g/46.0g/66.0g (Typ.)			
Cooling method	Free air convection				

Electron	nagnetic Compatibility (	(EMC)		
Emissions	CE	CISPR32/EN55032	CLASS A (without extra components)/ CLASS B (see Fig. 3-2) for recommended circuit)	
ETTISSIONS	RE	CISPR32/EN55032	CLASS A (without extra components)/ CLASS B (see Fig. 3-2) for recommended circuit)	
	ESD	IEC/EN61000-4-2	Contact ±4KV	perf. Criteria B
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A
	EFT	IEC/EN61000-4-4	±2KV (see Fig. 3-① for recommended circuit)	perf. Criteria B
Immunity	Surge	IEC/EN61000-4-5 circuit)	line to line ±2KV (see Fig. 3- $①$ for recommended)	perf. Criteria B
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A
	Voltage dips, short interruptions and voltage variations immunity	IEC/EN61000-4-29	0%, 70%	perf. Criteria B

## Typical Characteristic Curves

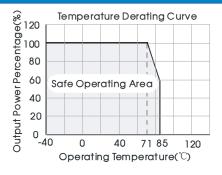


Fig. 1

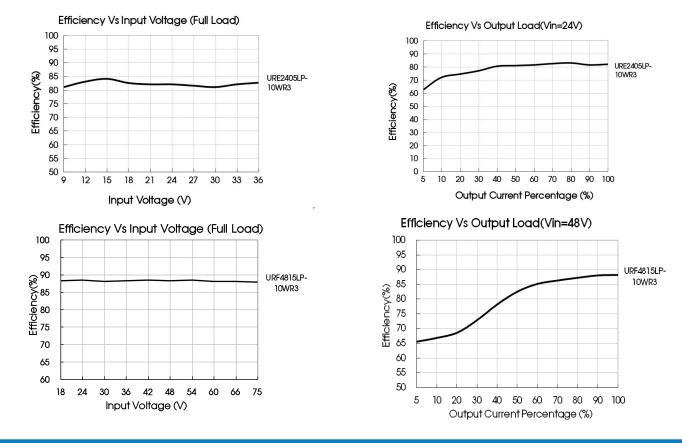


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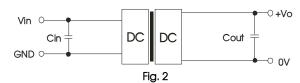




### **Design Reference**

#### 1. Typical application

All DC-DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 2. Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the max. capacitive load value of the product.



Cin	Cout
10µF - 47µF	10µF

#### 2. EMC compliance circuit

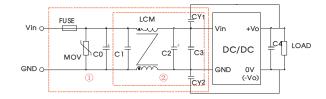


Fig. 3 Notes: For EMC tests we use Part  $\, \mathbb O$  in Fig. 3 for immunity and part  $\, \mathbb Q\,$  for emissions test.



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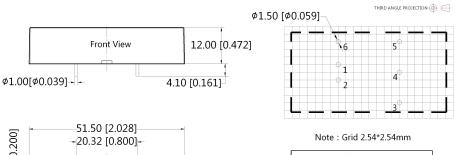


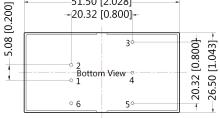
#### List of components:

Model	URE_L	P-10WR3	URF_LP-10WR3		
IVIODEI	Vin:24V	Vin:48V	Vin:24V	Vin:48V	
FUSE	· · · · ·	Choose according	to actual input current	·	
MOV	S20K30	S14K60	S20K30	S14K60	
C0	680µF/50V	680µF/100V	680µF/50V	680µF/100V	
C1	1µF/50V	1µF/100V	1µF/50V	1µF/100V	
C2	330µF/50V	330µF/100V	330µF/50V	330µF/100∨	
C3	4.7µF/50V	4.7µF/100V	4.7µF/50V	4.7µF/100V	
LCM	4.7mH, recommended to use MORNSUN's FL2D-30-472 6.8mH				
C4	Refer to the Cout in Fig.2				
CY1/CY2	1nF/3KV				

- 3. The products do not support parallel connection of their output
- 4. For additional information please refer to DC-DC converter application notes on www.mornsun-power.com

### **Dimensions and Recommended Layout**





Note: Unit :mm[inch] Pin diameter tolerances :±0.10[±0.004] General tolerances:±0.50[±0.020]

Pin-Out					
Pin	Single	Dual			
1	GND	GND			
2	Vin	Vin			
3	+Vo	+Vo			
4	No Pin	0V			
5	0V	-Vo			
6	Ctrl	Ctrl			

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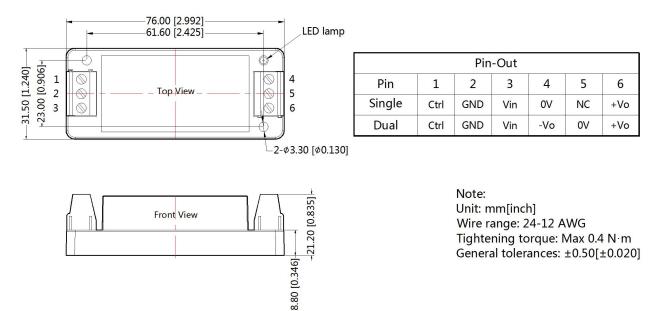
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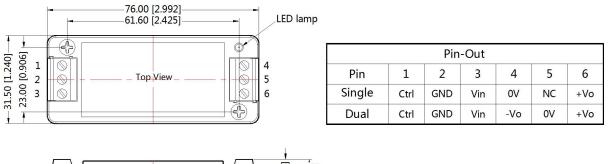
## URE\_LP-10WR3A2S & URF\_LP-10WR3A2S Dimensions

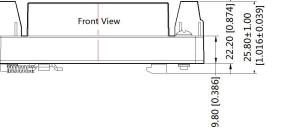
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### URE\_LP-10WR3A4S & URF\_LP-10WR3A4S Dimensions

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Note: Unit: mm[inch] Wire range: 24-12 AWG Tightening torque: Max 0.4 N·m General tolerances: ±0.50[±0.020]



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Note:

- 1. For additional information on Product Packaging please refer to www.mornsun-power.com. Packaging bag number: 58210039(DIP), 58220022(A2S/A4S package);
- 2. It is recommended that the load imbalance of the dual output is ≤±5%. If it exceeds ±5%, the performance of the product cannot be guaranteed to meet as datasheet marked. For details, please contact our technical staff;
- 3. The maximum capacitive load offered were tested at input voltage range and full load;
- 4. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 5. All index testing methods in this datasheet are based on company corporate standards;
- 6. We can provide product customization service, please contact our technicians directly for specific information;
- 7. Products are related to laws and regulations: see "Features" and "EMC";
- 8. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

# Mornsun Guangzhou Science & Technology Co., Ltd.

Address: No. 5, Kehui St. 1, Kehui Development Center, Science Ave., Guangzhou Science City, Luogang District, Guangzhou, P. R. ChinaTel: 86-20-38601850Fax: 86-20-38601272E-mail: info@mornsun.cnwww.mornsun-power.com

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