# **MORNSUN®**

#### 750W, AC-DC Brick Converter



#### **FEATURES**

- Ultra-wide 85 305VAC and 120 430VDC input voltage range
- Typical efficiency up to 92%, PF value up to 0.99
- International standard full brick package, aluminum substrate process
- PFC & DCDC converter integrated package
- Input under-voltage protection, over temperature protection, output short circuit/over-voltage/ over-current protection
- Output voltage adjustable
- Integrated parallel current sharing, status indication, remote control, auxiliary power supply, remote compensation function
- Designed to meet UL/IEC/EN62368, GB4943 standards

LBF750-13Bxx series is the Mornsun AC-DC brick package power supply, It features universal AC input and at the same time accepts DC input voltage, high power density, high efficiency, reinforced isolation. It offers good EMC performance compliant to CISPR32/EN55032, UL/IEC/EN62368 standards. The products are widely used in military, industrial control, data communication, network communication, server, vehicle/airborne/ship system and other industries. For extremely harsh EMC environment, we recommend using the application circuit show in Design Reference of this datasheet.

Selection Guide				
Part No.	Output Power	Nominal Output Voltage and Current(Vo/Io)	Efficiency at 230VAC (%) Typ.	Capacitive Load (uF) Max.®
LBF750-13B12	696W	12V/58A	89	5000
LBF750-13B24		24V/31.2A	91	4000
LBF750-13B28	750\\	28V/26.8A	91.5	3000
LBF750-13B48	750W	48V/15.6A	92	1000
LBF750-13B54		54V/13.9A	92	820

Input Specifications®						
Item	Operating Conditions		Min.	Тур.	Max.	Unit
Input Voltago Pango	AC input		85		305	VAC
Input Voltage Range	DC input		120		430	VDC
Input Frequency			47	-	63	Hz
B 5 l	115VAC	Room temperature,		PF≫	0.99	
Power Factor	230VAC	full load	PF≥0.96			
	115VAC		10			
Input Current	230VAC		5		5	
	Built-in inrush current	115VAC		20	_	Α
Inrush Current	suppression circuit, external $12\Omega$ power resistor	230VAC		40		
Input Under-voltage Protection	Under-voltage protection star	t	60		75	VAC
input offder-voltage Profection	Under-voltage protection rele	ase	75 - 85		85	, v, c
Remote Control Switch (ON/OFF)*	Power on	ON/OFF connect to 0 (0-1VDC) or				
Romoto Comito Swiich (ON/OH)	Power off		ON/OFF connect to AUX or High-level (3-14VDC)			or
Recommended External Input Fuse			15A/	300VAC, slov	v-blow, requ	iired

# AC/DC Converter

### LBF750-13Bxx Series



Hot Plug		Unavailable
Note: *Remote Control Switch (ON/OFF) co	ntrol the voltage of pin relative to pin COM.	

Output Specifications®						
Item	Operating Conditions		Min.	Тур.	Max.	Unit
Output Voltage Accuracy			-	±2		
Line Regulation	Full load			±0.5		%
Load Regulation	10% -100% load		-	±0.5		
		12V	-	180		mV
Ripple & Noise <sup>®</sup>	> 100/   00/ 4/	24V		180		
	≥10% Io, 20MHz bandwidth (peak-to-peak value)	28V		200		
	(pour le pour value)	48V		340		
		54V		380		
Stand-by Power Consumption	Room temperature, 0% lo, 230	VAC input voltage	-	3		W
Hold-up Time	115/230VACinput		-	8		ms
Minimum Load®			10			%
Output Voltage Adjustable (Trim)			90		110	
Output Voltage Remote (Sense)			-	-	105	%Vo
Parallel (PC)	PC to COM in parallel			Support direct parallel use, achieve N+1 parallel redundancy		
Current Staring Accuracy	Output >50% lo		_		5	%
Auxiliary Source Supply (AUX)	lo=50mA		10		14	V
10001-1-1-1	Normal output		Output: Pulse			
IOG Status Indication	Abnormal output		Output: High-level			
Short Circuit Protection			Hicc	Hiccup or turn off, self-recover		
Over-current Protection			105% - 170% lo, self-recover			er
	12VDC output		≤20VDC (Hiccup or clamp)			5)
	24VDC output		≤35VDC (Hiccup or clamp)			
Over-voltage Protection	28VDC output		≤35VDC (Hiccup or clamp)			<b>o</b> )
• • • • • • • • • • • • • • • • • • • •	48VDC output		≤65	3VDC (Hicc	up or clamp	<b>o</b> )
	54VDC output		≤70	OVDC (Hicc	up or clamp	o)
Over Temperature Protection	Over-temperature protection temperature) until power of		105	-	130	$^{\circ}$
eta lampararara i lamanari	Over-temperature protection	recovery	Power	off, BOOST discharge, resta		estart

Note: 1. Tip and barrel method" is used for ripple and noise test, please refer to AC-DC Converter Application Notes for specific information; 2. The product is able to work stably at load of 0% - 10%; 3. The above specifications are tested based on the rated input voltage.

General S	pecifications					
Item		Operating Conditions	Min.	Тур.	Max.	Unit
Input - Output			3000			
Isolation Input - PE	Input - PE	Electric Strength Test for 1min., leakage current <5mA	2500			VAC
Output - PE		leakage culietii <5mA	1500			
	Input - Output	Ta=25±5℃,	100			<b>Μ</b> Ω
Insulation Resistance Input - PE Output - PE	Input - PE	Relative humidity: <95%RH, non-condensing Testing voltage: 500VDC	100			
	Output - PE		100			
Al-Substrate Temperature			-40		+100	$^{\circ}$
Storage Temperature			-40		+100	C
Storage Humidity		Non-condensing			95	%RH
Soldering Temperature		Wave-soldering		260 ± 5°C; time: 5 - 10s		
		Manual-welding		360 ± 10°C; time: 3 - 5s		

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MORNSUN Guangzhou Science & Technology Co., Ltd.

# AC/DC Converter

## LBF750-13Bxx Series

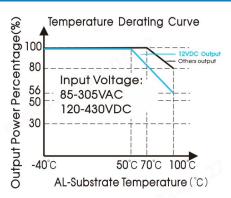


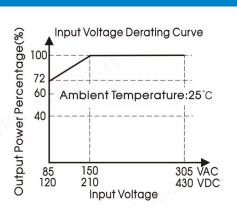
Switch Frequency				130		kHz
Power Derating	Al-Substrate	+50°C to +100°C (12V output)	0.88		O/ /°C	9/ /°∩
	Temperature	+70°C to +100°C (other output)	0.67			<b>- %/</b> ℃
	Input voltage	85VAC - 150VAC	0.43	-		%/VAC
	Altitude	2000m - 5000m	6.67	-		℃/Km
Safety Standard			Design refe	r to UL/IEC/E	N62368-1,	GB4943.1
Safety Class			CLASS I			
MTBF	MIL-HDBK-217F@2	MIL-HDBK-217F@25°C				

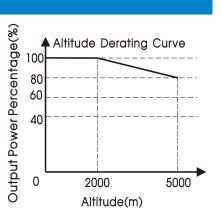
Mechanica	al Specifications	
Case Material		Aluminum substrate+black plastic, flame-retardant and heat-resistant (UL94V-0)
Dimension	DIP	116.80 x 61.00 x 12.70mm
Weight	DIP	260g (Typ.)
Cooling method	1	Conduction heat dissipation, it is necessary to ensure that the product aluminum substrate surface temperature lower than 100°C.

<b>C</b> F	CISPR32/EN55032	2 CLASS A				
CE	CE102 GJB151B	(See Fig. 2 for recommended circuit )				
RE	CISPR32/EN55032	CLASS A				
Harmonic current	EN61000-3-2	CLASS A				
THD	EN61000-3-2	≤8%				
ESD	IEC/EN61000-4-2	Contact ±6KV/Air ±8KV	perf. Criteria B			
RS	IEC/EN61000-4-3	10V/m	perf. Criteria A			
EFT	IEC/EN61000-4-4	±2KV	perf. Criteria A			
Surge	IEC/EN61000-4-5	Line to line ±1KV/line to PE ±2KV	perf. Criteria A			
CS	IEC/EN61000-4-6	10Vr.m.s	perf. Criteria A			
MS	IEC/EN61000-4-8	10A/m	perf. Criteria A			
Voltage dip, short interruption and voltage variation	IEC/EN61000-4-11	0%, 70%	perf. Criteria B			
	Harmonic current THD ESD RS EFT Surge CS MS Voltage dip, short interruption and voltage variation	RE         CISPR32/EN55032           Harmonic current         EN61000-3-2           THD         EN61000-3-2           ESD         IEC/EN61000-4-2           RS         IEC/EN61000-4-3           EFT         IEC/EN61000-4-4           Surge         IEC/EN61000-4-5           CS         IEC/EN61000-4-6           MS         IEC/EN61000-4-8           Voltage dip, short interruption and voltage variation         IEC/EN61000-4-11	RE         CISPR32/EN55032         CLASS A           Harmonic current         EN61000-3-2         CLASS A           THD         EN61000-3-2         ≤8%           ESD         IEC/EN61000-4-2         Contact ±6KV/Air ±8KV           RS         IEC/EN61000-4-3         10V/m           EFT         IEC/EN61000-4-4         ±2KV           Surge         IEC/EN61000-4-5         Line to line ±1KV/line to PE ±2KV           CS         IEC/EN61000-4-6         10Vr.m.s           MS         IEC/EN61000-4-8         10A/m           Voltage dip, short interruption         IEC/EN61000-4-11         0% 70%			

## **Product Characteristic Curve**



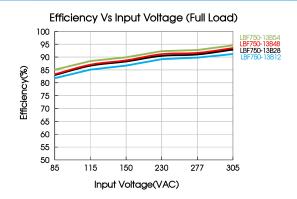


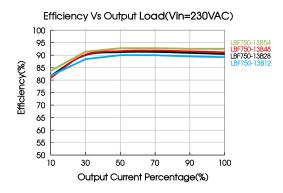


#### Note:

- ① With an AC input voltage between 85 150VAC/120 210VDC the output power must be derated as per the temperature derating curves;
- $\ensuremath{@}$  The temperature derating curve is a typical test value, the working condition is heat sink with air cooling.

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#### Additional Circuits Design Reference

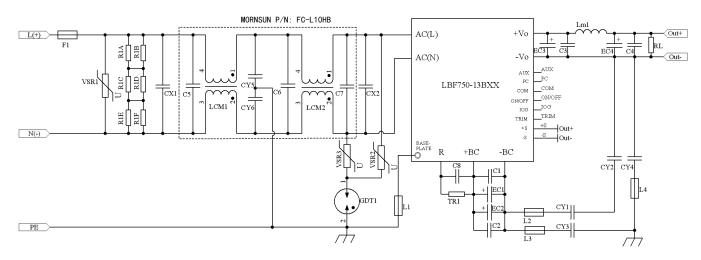


Fig. 1: Recommended circuit 1

Component		Recon	nmended value		
Fl		300VAC/15A, show-blow			
VSR1/VSF	R2/VSR3	\$14K350/6000A			
R1A/R1B/R1C/	RID/RIE/RIF	24	<b>40K</b> Ω/1 <b>20</b> 6		
CX1/	CX2	22	25K/310VAC		
	C5/C6/C7	10	94K/310VAC		
MORNSUN P/N:	CY5/Cy6	Y2/2	222M/250VAC		
FC-L10HB*	LCM1		1.8mH		
	LCM2		500uH		
GDT1		800V/5KA			
11/12/13/14		•	$7\Omega$ /DCR $0.004\Omega$ Max high frequency beads)		
C1/	C8		683K/630V		
C	2	472	K/2000V/1206		
TR	1	12 º /20W	//wire-round resistor		
CY1/	CY2	Y2/4	172M/250VAC		
EC1/	EC2	420uF/450V (Alumi	inum Electrolytic Capacitor)		
CY	/3	Y2/2	222M/250VAC		
CY4		Y2/4	471M/250VAC		
1	.1	12V	0.33uH/0.35m Ω Max/80A(RKR0620)		
Lm	11	24V/28V/48V/54V	short-circuit or $0$ m $\Omega$ resistor		
EC4		12V	6000uF/16V (Aluminum Electrolytic Capacitor)		



EC3/EC4	24V/28V	2000uF/35V (Aluminum Electrolytic Capacitor)
	48V	1000uF/63V (Aluminum Electrolytic Capacitor)
	54V	820uF/100V (Aluminum Electrolytic Capacitor)
00/04	12V/24V/28V	106K/50V/1206
C3/C4	48V/54V	225K/100V/1206
	12V	125 Ω /3W
RL	24V/28V	0.5K Ω /3W
	48V/54V	2KΩ/3W

Note: \*P/N: FC-L10HB (MORNSUN) is preferred, the effect of the self-built circuit is greatly affected by magnetic material and layout.

#### Conducted Emission (CE102) Recommended Circuit

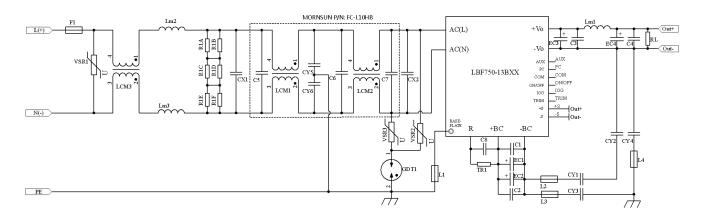
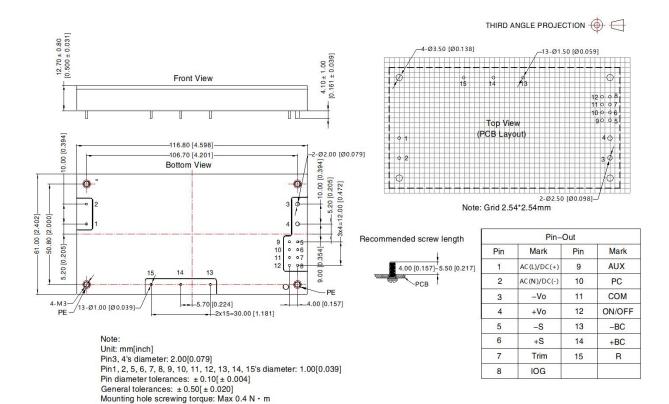


Fig. 2: Recommended circuit 2

Component	Recommended value
LCM3	5.6mH/Min: 4A
Lm2/Lm3	2mH/Min: 4A
CX1	225K/310VAC
CX2	335K/310VAC



#### **Dimensions and Recommended Layout**



#### Note:

- 1. For additional information on Product Packaging please refer to <a href="www.mornsun-power.com">www.mornsun-power.com</a>. Packaging bag number: 58210118;
- 2. If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75% with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on our company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. If product involves multi-brand materials and there are differences in color etc, please refer to the standards of each manufacturer;
- Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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# LBF750-13BXX Series Power Supply Application Manual

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## 1. Performance Characteristics And Appearance Pin Definition

#### 1.1. Performance characteristics

- Ultra-wide 85 305VAC and 120 430VDC input voltage range
- Typical efficiency up to 92%, PF value: 0.99
- Integrated parallel current sharing, IOG status indication, remote control, auxiliary power supply, remote compensation function
- Input under-voltage protection, output short circuit, over-voltage, over-current protection, over temperature protection
- Designed to meet UL/IEC/EN62368, GB4943 standards
- International standard full brick package, aluminum substrate process
- Built-in capacitor: no electrolytic, only ceramic capacitor (high reliability)

#### 1.2. Appearance pin definition



Figure 1: Appearance pins

Terminal name	Terminal definition			
AC(L)/DC(+)	AC input L line/DC input +			
AC(N)/DC(-)	AC input N line/DC input -			
-VO	Output voltage negative			
+VO	Output voltage positive			
-S	Output voltage negative end remote compensation			
+S	Output voltage positive and remote compensation			
TRIM	Output voltage adjustable terminal			
IOG	Output status indicating terminal			
AUX	Auxiliary power terminal for external signal			
PC	Modules run in parallel			

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ON/OFF	Remote control switch
COM	Common terminal
-BC	- Boost terminal (external electrolytic capacitor negative terminal)
+BC	+ Boost terminal (external electrolytic capacitor positive terminal)
R	Terminals for external resistors that limit input surge current

#### 2. Instructions For Use

#### 2.1. Input requirements

The AC input voltage and DC input voltage must be within the defined voltage range (refer to datasheet), otherwise the power supply may not work properly or even malfunction. There is no fuse inside the power module. For better protection, it is recommended that customers use a circuit breaker not greater than 15A.

To ensure the reliability of the product, hot plugging is prohibited.

#### 2. 2. Output requirements

At any voltage value, the maximum output current and power must not exceed the rated value.

#### 2.3. Remote compensation (+S, -S terminals)

As shown in Figure 2, the +S and -S terminals are respectively connected to the load terminal (Vo+ and Vo-) through twisted-pair signal cables or differential signal cables (+S and -s) to compensate the line voltage drop between the module and the load. It should be noted that the +S and -S terminals must be connected to the load terminal through signal cables before the module is powered on, otherwise the module will enter over-voltage protection; If the remote compensation connection is not required, the +S and -S terminals can be shorted to the output positive and negative (+Vo and -Vo terminals) respectively.

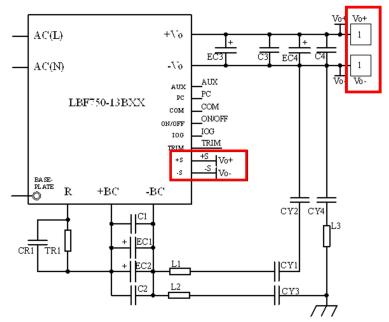


Figure 2: Schematic diagram of the remote compensation connection



#### 2.4. Output voltage adjustment (Trim terminal)

As shown in the wiring diagram in the red box in Figure 3, the output voltage of the module can be adjusted within  $\pm 10\%$  of the rated output voltage by connecting the external resistor R1 and sliding rheostat RV. When the output voltage is higher than the adjustable range, it may cause output over-voltage protection. When the output voltage increases, reduce the output current to ensure that the maximum output power of the module stays within the specified range. When the output voltage is lowered, the maximum output current remains unchanged.

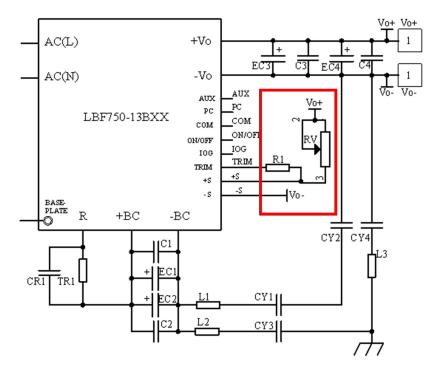


Figure 3: Schematic diagram of the output voltage regulation connection

Trim resistance calculation formula: Up:

$$RZ = \frac{R4 \cdot \left[ Voup - \frac{Vref \cdot (R3 + R4)}{R4} \right]}{Vref}$$

Note: R1 left open. Down:

$$R1 = -\frac{R3 \cdot R4 \cdot Vref - R3 \cdot R4 \cdot Vodown}{R3 \cdot Vref + R4 \cdot Vref - R4 \cdot Vodown}$$

Note: RV short connection.

Vout	R3	R4	Vref					
12V	<b>10k</b> Ω	<b>2.629k</b> Ω	2.5V					
24V	<b>33k</b> Ω	3.83k Ω	2.5V					
28V	<b>24k</b> Ω	<b>2.35k</b> Ω	2.5V					
48V	<b>100k</b> Ω	<b>5.4862k</b> Ω	2.5V					
54V	<b>63k</b> Ω	<b>3.3k</b> Ω	2.5V					
Note: R3, R4 are built-in resist	Note: R3, R4 are built-in resistors, and Vref is internal reference.							



Recommended value of Trim resistor (adjustable slip RZ to achieve upper and lower output voltage regulation):

Vout	R1	RV
12V	<b>56k</b> Ω	Sliding rheostat with adjustable range of 0-2.9k $\Omega$
24V	<b>220k</b> Ω	Sliding rheostat with adjustable range of 0-8.6k $\Omega$
28V	<b>150k</b> Ω	Sliding rheostat with adjustable range of 0-6.4k $\Omega$
48V	<b>750k</b> Ω	Sliding rheostat with adjustable range of 0-23.2k $\Omega$
54V	<b>510k</b> Ω	Sliding rheostat with adjustable range of 0-15.9k $^{\Omega}$

Note: When the sliding rheostat adjustment resistance exceeds the recommended adjustable range too much, the module will enter over-voltage protection.

#### 2.5. Remote control switch (ON/OFF terminal)

Module built-in remote control switch function. This function can be realized in the input voltage on state control output on/off. The wiring diagram is shown in Figure 4. You can directly supply power to ON/OFF terminals through AUX terminals. When using external power supply to ON/OFF, the external power supply voltage does not exceed 15V. If the remote switch function is not used, hang ON/OFF or connect to COM or low level (0-1VDC).

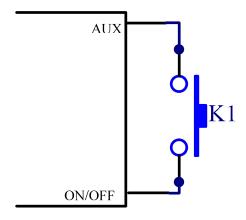


Figure 4: Schematic diagram of remote switch connection

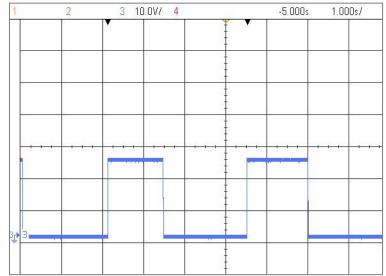


Figure 5: Output voltage diagram of ON/OFF function



#### 2.6. Parallel operation (PC terminal)

The PC terminal is a parallel current sharing bus. Parallel-connect the PC and COM terminals of each power module to equalize the output current between modules. At the output end of the power supply, the output cable width and length of each module should be as consistent as possible, and the line impedance should be as similar as possible. After the output filtering of a single module, a load bus is drawn from the load end. After the output filtering, each module accesses the load bus nearby through the load line of the same specification and length, and the mobility is optimal. Parallel operation connection is shown in Figure 6 below.

Power modules support 4+1 parallel redundancy.

When the system is used in parallel, the maximum load current cannot exceed the maximum output current of a single power module; otherwise, the entire parallel power system cannot start properly.

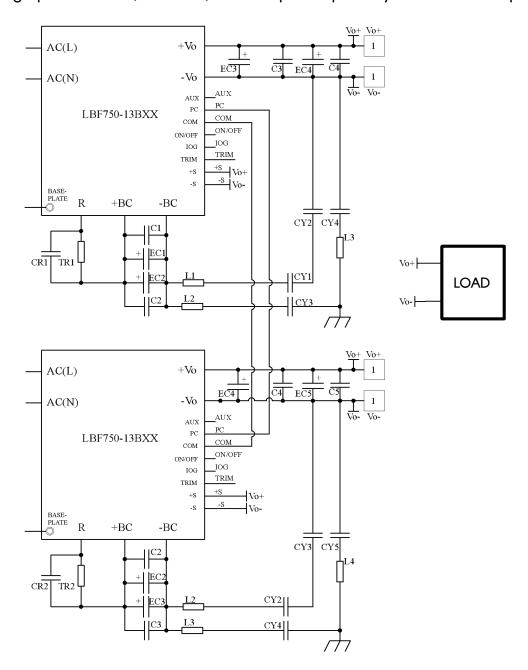


Figure 6: Diagram of parallel operation connection



#### 2.7. Auxiliary power supply for external signals (AUX terminal)

The AUX terminal output voltage ranges from DC10V to DC14V, and the maximum output current is 50mA. AUX terminal reference position COM terminal. Do not short-circuit the AUX terminal to a terminal other than the ON/OFF terminal. Otherwise, the power module may be damaged.

#### 2.8. IOG status indicator

The signal is the output signal of the module and the reference ground is the COM terminal. By monitoring the signal from IOG terminal to COM terminal, you can check whether the power module is working properly. Pulse signal when working normally, high level when working abnormally.

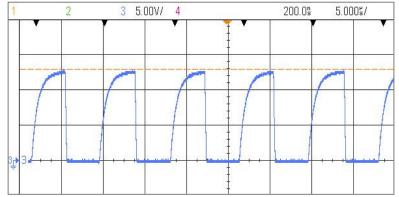


Figure 7: IOG status indication waveform in normal operation

Note: Normal output is "pulse", in case of failure, output "H" (maximum pull-down current is 5mA, maximum applied voltage is 35V).

#### 2.9. Input under-voltage protection (UVP)

When the input voltage is lower than the under-voltage protection set value, the module output is closed; When the input voltage is higher than the under-voltage protection power-on set value, the module output is normal. The under-voltage protection has a return difference, that is, the shutdown setting value is lower than the start-up setting value, so as to prevent the module from being affected by external interference or the transient drop of input voltage when starting itself and working normally.

#### 2. 10. Output over-voltage protection (OVP)

This module has output clamp type output over-voltage protection function. When the output end of the module is over-voltage, the output voltage clamped at a fixed value or hiccup. After the fault is rectified, the module output automatically recovers to normal.

#### 2.11. Over-current/short circuit protection

This module is designed with over-current/short circuit protection circuit, which can withstand over-current or short circuit at the output end. In short circuit state, the module is in belch state (200ms at work, 2s at rest), as shown in Figure 8. In case of short circuit with load cutting, the module can enter the rest state after constant current for 1s, as shown in Figure 9; After the over-current and short circuit faults are eliminated, the module output automatically recovers to normal.

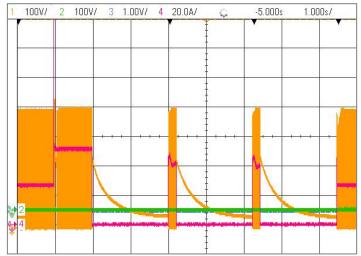


Figure 8: Waveform diagram, full load and short circuit switching

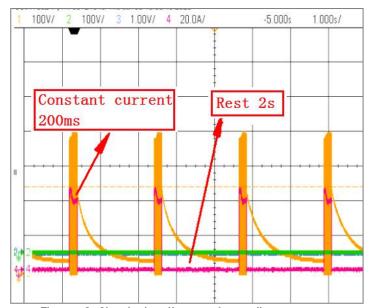


Figure 9: Short circuit waveform diagram

Note: Red indicates the output current.

#### 2.12. Over-temperature protection

The built-in over-temperature protection circuit of the module prevents the module from being damaged due to excessive temperature rise such as overload and short circuit. When the temperature of the module shell exceeds the set value of over-temperature protection, the output of the module automatically closes. You need to power off and reset to restore the system.

#### 2.13. Output power derating

When the input voltage is greater than 150VAC (or 210VDC), only need to derate according to the temperature derating curve.

When the input voltage is lower than 150VAC (or 210VDC), the output power will be derated according to the following input voltage derating curve after temperature derating;

The temperature derating curve is a typical test value, the working condition is heat sink with air cooling.

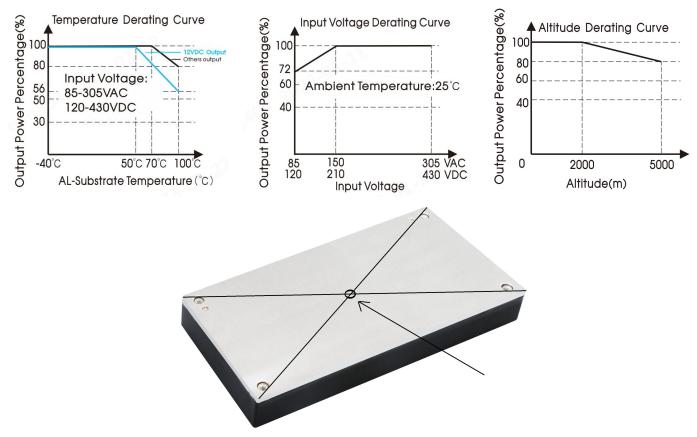


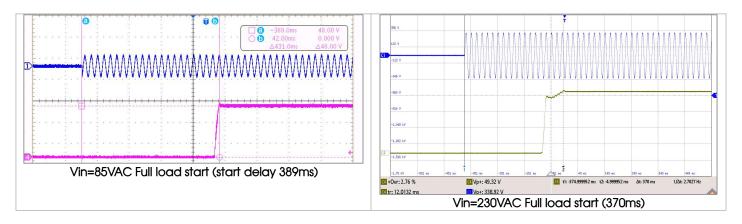
Figure 10: AL-Substrate temperature test point

Note: The test point of Al-Substrate temperature is the temperature of the center point of the substrate.

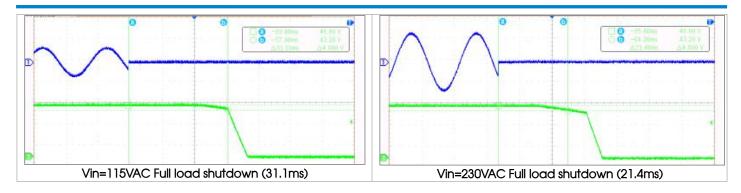
## 3. Test Waveform

#### 3. 1. Switch ON/OFF

Test conditions: Tc=25  $^\circ$  , LBF750-13B48 products are tested based on recommended circuit 1, EC1/EC2=360~420uF.



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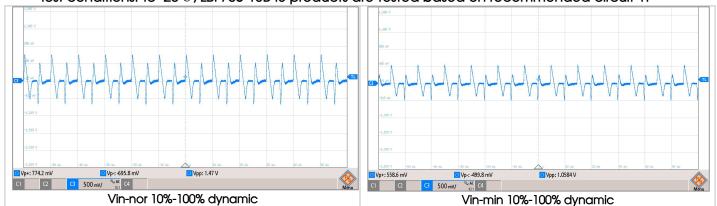
Note: The power OFF hold-up time is related to the EC capacitance, and can be adjusted with reference to the following formula:

$$t = \frac{0.5 \cdot C_{EC} \cdot (U1^2 - U2^2)}{P_O}$$

U1=396VDC, U2=309VDC, Po=750W (based on actual power output).

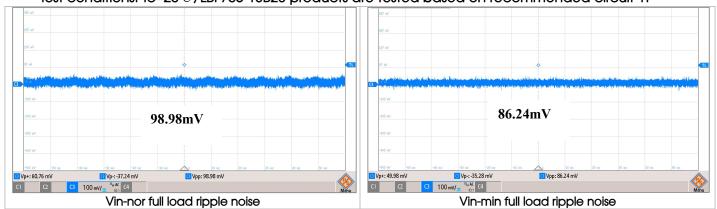
#### 3.2. Dynamic response

Test conditions: Tc=25℃, LBF750-13B48 products are tested based on recommended circuit 1.



#### 3.3. Output ripple and noise

Test conditions: Tc=25℃, LBF750-13B28 products are tested based on recommended circuit 1.



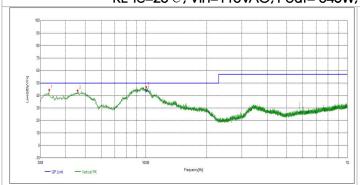


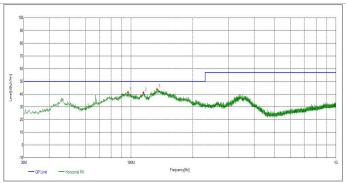
#### 3.4. Conductive and radiation (EMI)

#### (1) Radiation (RE):

Safety specifications: CISPR32/EN55032 CLASS A

#### RETc=25°C, Vin=115VAC, Pout=640W, based on recommended circuit 1 test





	Suspected List									
Frequency [MHz]	Polarity	Factor [dB]	Readin g [dBµV/ m]	Level [dBµV/ m]	Limit [dBµV/ m]	Margin [dB]	Detect	Height [cm]	Angle deg	Pass/ Fail
33.2013	Vertical	20.46	24.03	44.49	50.00	5.51	PK	100	138	PASS
45.9096	Vertical	20.82	23.16	43.98	50.00	6.02	PK	100	190	PASS
100.4290	Vertical	20.03	26.82	46.85	50.00	3.15	PK	100	203	PASS

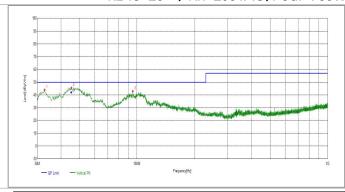
	Final Data List							
Frequency [MHz]	Polarity	Factor [dB]	QP Value [dBµV/m]	QP Limit [dBµV/m]	QP Margin [dB]	Height [cm]	Angle [°]	Pass/Fa il
100.3070	Vertical	20.03	43.68	50.00	6.32	280	203	PASS

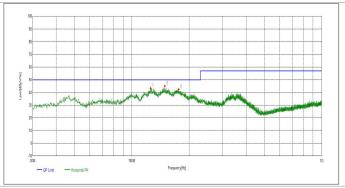
	Suspected List									
Frequency [MHz]	Polarity	Factor [dB]	Readin g [dBµV/ m]	Level [dBµV/ m]	Limit [dBµV/ m]	Margin [dB]	Detect	Height [cm]	Angle deg	Pass/ Fail
96.6457	Horizontal	19.57	22.02	41.59	50.00	8.41	PK	100	118	PASS
114.8835	Horizontal	18.32	22.88	41.20	50.00	8.80	PK	100	111	PASS
133.8974	Horizontal	16.32	27.61	43.93	50.00	6.07	PK	100	92	PASS

Vertical waveform and reading point

Horizontal waveform and reading point

#### RE Tc=25 $^{\circ}$ C, Vin=230VAC, Pout=750W, based on recommended circuit 1 test





	Suspected List									
Frequency [MHz]	Polarity	Factor [dB]	Readin g [dBµV/ m]	Level [dBµV/ m]	Limit [dBµV/ m]	Margin [dB]	Detect or	Height [cm]	Angle deg	Pass/ Fail
32.8133	Vertical	20.42	23.56	43.98	50.00	6.02	PK	100	196	PASS
45.3275	Vertical	20.86	24.71	45.57	50.00	4.43	PK	100	242	PASS
95.1905	Vertical	19.35	23.63	42.98	50.00	7.02	PK	100	229	PASS

	Final Data List							
Frequency [MHz]	Polarity	Factor [dB]	QP Value [dBµV/m]	QP Limit [dBµV/m]	QP Margin [dB]	Height [cm]	Angle [°]	Pass/Fa il
45.1989	Vertical	20.85	42.03	50.00	7.97	220	242	PASS

		Suspected List									
	Frequency [MHz]	Polarity	Factor [dB]	Readin g [dBµV/ m]	Level [dBµV/ m]	Limit [dBµV/ m]	Margin [dB]	Detect	Height [cm]	Angle deg	Pass/ Fail
	125.5546	Horizontal	17.16	26.21	43.37	50.00	6.63	PK	100	245	PASS
[	149.2249	Horizontal	15.83	29.30	44.93	50.00	5.07	PK	100	239	PASS
[	176.4846	Horizontal	16.79	25.60	42.39	50.00	7.61	PK	100	258	PASS

Vertical waveform and reading point

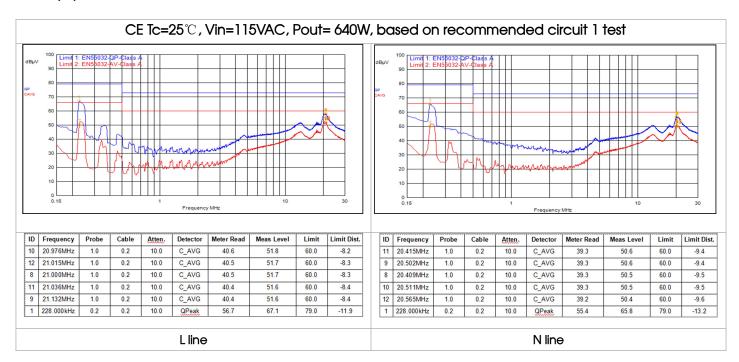
Horizontal waveform and reading point

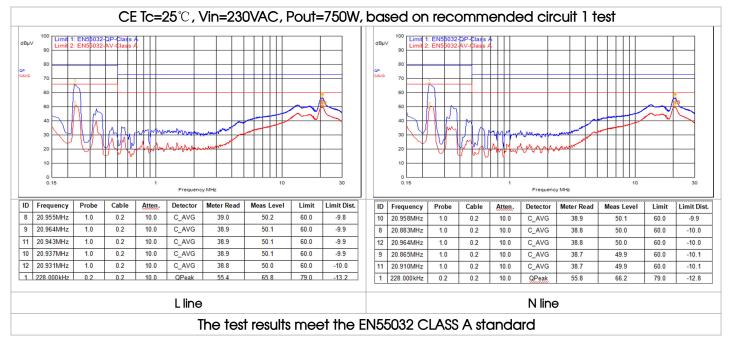
The test results meet CLASS A standards



(2) Conductive (CE):

Safety specifications: CISPR32/EN55032 CLASS A

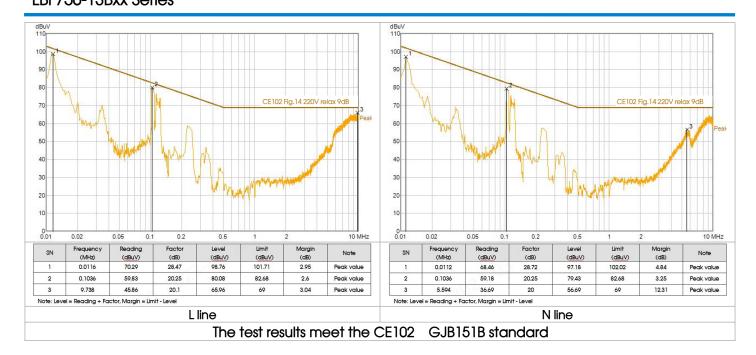




Safety specifications: CE102 GJB151B

CE Tc=25°C, Vin=220VAC, Pout=750W, based on recommended circuit 2 test

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## 4. Appearance Specifications

#### 4.1. Manufacturing data/dimensions

 Length:
 116.8mm±0.5mm

 Width:
 61.0mm±0.5mm

 Height:
 12.7mm±0.5mm

 Terminal length:
 4.1mm±0.5mm

 Weight:
 260g±10%g

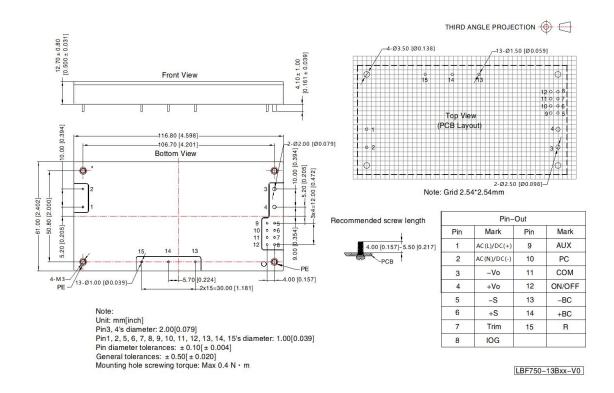


Figure 11: Manufacturing data/size diagram



#### 4.2. Installation and disassembly methods

Installation method: Place the heat sink on the aluminum base plate and fasten the heat sink to the product using four screws.

Removing method: Use a tool to separate the four screws from the heat sink.

Maximum mounting hole tightening torque: 0.4N.m.

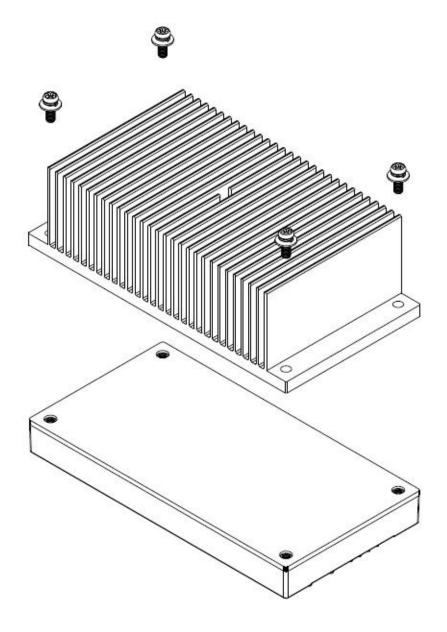


Figure 12: Installation and disassembly of product and heat sink

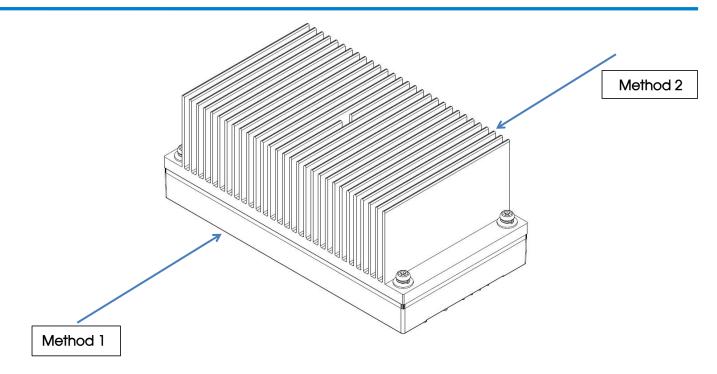


Figure 13: Schematic diagram of finished product after installation and instruction of wind direction

#### 4.3. Cooling method

	Surface heat sink	
Logt dissipation mother	Natural cooling	
Heat dissipation method	Conduction heat dissipation	
	In this document, "■" indicates selecte	ed, and "□" indicates not selected

For more details, please consult the MORNSUN FAE.